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<u>L7</u>	11 and interrupt\$ near4 (re-direct\$ or direct\$ or transfer\$) near4 replac\$	1	<u>L7</u>
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<u>L5</u>	ll and replac\$ near9 (disabl\$ or interrrupt\$)	1	<u>L5</u>
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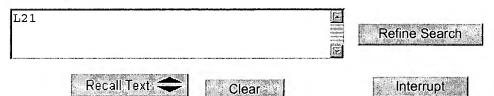
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<u>L21</u>	711/103.CCLS.	738	<u>L21</u>
DB=	TDBD; PLUR=YES; OP=ADJ		
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DB=	*JPAB; PLUR=YES; OP=ADJ		
<u>L18</u>	(modif\$ or updat\$ or chang\$ or alter\$) near4 program\$ and (redirect\$ OR TRANSFER\$ OR TRANSMIT\$) and address and (mark\$ or tag\$) near5 address\$ AND REPLAC\$ NEAR4 (INVALID\$ OR DISABL\$)	0	<u>L18</u>
DB=	EPAB; PLUR=YES; OP=ADJ		

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<u>L17</u>	(modif\$ or updat\$ or chang\$ or alter\$) near4 program\$ and (redirect\$ OR TRANSFER\$ OR TRANSMIT\$) and address and (mark\$ or tag\$) near5 address\$ AND REPLAC\$ NEAR4 (INVALID\$ OR DISABL\$)	0	<u>L17</u>
DB=	FPGPB; PLUR=YES; OP=ADJ		
<u>L16</u>	(modif\$ or updat\$ or chang\$ or alter\$) near4 program\$ and (redirect\$ OR TRANSFER\$ OR TRANSMIT\$) and address and (mark\$ or tag\$) near5 address\$ AND REPLAC\$ NEAR4 (INVALID\$ OR DISABL\$)	13	<u>L16</u>
L15	L8 AND REPLAC\$ NEAR4 (INVALID\$ OR DISABL\$)	0	<u>L15</u>
DB=	USPT; PLUR=YES; OP=ADJ		
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<u>L7</u>	L6 AND L1	1	<u>L7</u>
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<u>L5</u>	12 and 11	0	<u>L5</u>
<u>L4</u>	L3 and 11	0	<u>L4</u>
<u>L3</u>	L2 and (invalid\$ or disabl\$)	70	<u>L3</u>
<u>L2</u>	(modif\$ or updat\$ or chang\$ or alter\$) near4 program\$ and redirect\$ and address and (mark\$ near5 address\$)	82	<u>L2</u>
<u>L1</u>	717/168,129,127.ccls.	504	<u>L1</u>

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Relevance scale

Continuous program optimization: A case study

Thomas Kistler, Michael Franz

July 2003 ACM Transactions on Programming Languages and Systems (TOPLAS), Volume 25 Issue 4

Full text available: pdf(877.67 KB)

Additional Information: full citation, abstract, references, index terms

Much of the software in everyday operation is not making optimal use of the hardware on which it actually runs. Among the reasons for this discrepancy are hardware/software mismatches, modularization overheads introduced by software engineering considerations, and the inability of systems to adapt to users' behaviors. A solution to these problems is to delay code generation until load time. This is the earliest point at which a piece of software can be fine-tuned to the actual capabilities of the ...

Keywords: Dynamic code generation, continuous program optimization, dynamic reoptimization

2 Enabling trusted software integrity

Darko Kirovski, Milenko Drinić, Miodrag Potkonjak

October 2002 Proceedings of the 10th international conference on architectural support for programming languages and operating systems, Volume 37, 30, 36 Issue 10, 5, 5

Full text available: pdf(1.39 MB)

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Preventing execution of unauthorized software on a given computer plays a pivotal role in system security. The key problem is that although a program at the beginning of its execution can be verified as authentic, while running, its execution flow can be redirected to externally injected malicious code using, for example, a buffer overflow exploit. Existing techniques address this problem by trying to detect the intrusion at run-time or by formally verifying that the software is not prone to a p ...

3 Simultaneous subordinate microthreading (SSMT)

Robert S. Chappell, Jared Stark, Sangwook P. Kim, Steven K. Reinhardt, Yale N. Patt May 1999 ACM SIGARCH Computer Architecture News, Proceedings of the 26th annual international symposium on Computer architecture, Volume 27 Issue 2

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